

IN THE CLAIMS:

Please cancel Claim 29 without prejudice or disclaimer of the subject matter recited therein.

Please amend Claims 30 and 32-35 and 37 as follows.

1. to 29. (Cancelled).

30. (Currently Amended) A drive circuit ~~according to claim 29~~, comprising:

a plurality of current signal generation circuits for outputting a current signal to each of a plurality of display units;

a current signal output line to which outputs of said plurality of current signal generation circuits are commonly connected;

a control circuit for controlling each of said plurality of current signal generation circuits to be a current signal output state capable of evaluating an output of one of said plurality of current signal generation circuits;

a correction value output circuit for evaluating the output of the one of said plurality of current signal generation circuits on a basis of the current values output through said current signal output line to output a correction value according to an evaluation result; and

a correction circuit for correcting an image signal supplied to said plurality of current signal generation circuits by means of the correction value, wherein

said control circuit supplies a predetermined signal to the one of said plurality of current signal generation circuits, and supplies a signal different from the predetermined signal to the other current signal generation circuits.

31. (Previously Presented) A drive circuit according to claim 30, wherein the different signal is a signal such that a current value of a current signal output from each of the other or others of the current signal generation circuits, to which the different signal has been supplied, is made smaller than a current value of the current signal output from said one of said current signal generation circuits.

32. (Currently Amended) A device circuit according to claim ~~29~~ 30, further comprising a switch for realizing a state in which said current signal output line is connected to said plurality of current signal generation circuits simultaneously.

33. (Currently Amended) A device circuit ~~according to claim 29, comprising:~~
a plurality of current signal generation circuits for outputting a current signal to
each of a plurality of display units;
a current signal output line to which outputs of said plurality of current signal
generation circuits are commonly connected;
a control circuit for controlling each of said plurality of current signal
generation circuits to be a current signal output state capable of evaluating an output of one of
said plurality of current signal generation circuits;

a correction value output circuit for evaluating the output of the one of said plurality of current signal generation circuits on a basis of the current values output through said current signal output line to output a correction value according to an evaluation result;

a correction circuit for correcting an image signal supplied to said plurality of current signal generation circuits by means of the correction value; and

further comprising a plurality of switches for controlling connection relations between said plurality of current signal generation circuits and said current signal output line, said plurality of switches being controlled by a common control signal.

34. (Currently Amended) A drive circuit ~~according to claim 29~~, comprising:

a plurality of current signal generation circuits for outputting a current signal to each of a plurality of display units;

a current signal output line to which outputs of said plurality of current signal generation circuits are commonly connected;

a control circuit for controlling each of said plurality of current signal generation circuits to be a current signal output state capable of evaluating an output of one of said plurality of current signal generation circuits;

a correction value output circuit for evaluating the output of the one of said plurality of current signal generation circuits on a basis of the current values output through said current signal output line to output a correction value according to an evaluation result;

a correction circuit for correcting an image signal supplied to said plurality of current signal generation circuits by means of the correction value; and

further comprising a plurality of switches for severally controlling connection relations between said plurality of current signal generation circuits and said output units, said plurality of switches being controlled by a common control signal.

35. (Currently Amended) A drive circuit according to claim 29, comprising:
a plurality of current signal generation circuits for outputting a current signal to each of a plurality of display units;
a current signal output line to which outputs of said plurality of current signal generation circuits are commonly connected;
a control circuit for controlling each of said plurality of current signal generation circuits to be a current signal output state capable of evaluating an output of one of said plurality of current signal generation circuits;
a correction value output circuit for evaluating the output of the one of said plurality of current signal generation circuits on a basis of the current values output through said current signal output line to output a correction value according to an evaluation result; and
a correction circuit for correcting an image signal supplied to said plurality of current signal generation circuits by means of the correction value, wherein
each of said plurality of current signal generation circuit circuits includes a circuit for outputting a current signal having a squared value of a value of an input signal, and said correction value output circuit outputs a correction value obtained by calculating a square root of a ratio of an output evaluation value of said each of said plurality of current said-current signal generation circuits circuit to a reference value.

36. (Previously Presented) A drive circuit according to claim 35, wherein said correction value output circuit includes a calculation circuit for calculating the square root, and the calculation is an approximation calculation performed by classifying according to a value of the ratio of the output evaluation value to the reference value.

37. (Currently Amended) An evaluation method of a drive circuit including a plurality of current signal generation circuits for outputting a current signal to each of a plurality of display units, comprising steps of:

connecting outputs of the plurality of current signal generation circuits to a common current signal output line;

controlling the plurality of current signal generation circuits to a current signal output state in which an output of one of the plurality of current signal generation circuits can be evaluated;

evaluating an output of the one of the plurality of current signal generation circuits on a basis of the current values output through the current single output line and outputting a correction value according to the evaluation result; and

correcting an image signal supplied to the plurality of current signal generation circuits by the correction value, wherein the controlling step includes a step of supplying a predetermined signal to the one of the plurality of current signal generation circuits, and a step of supplying a signal different from the predetermined signal to the other current signal generation circuits.